

Notic of R ferenc s Cited	Application/Control No. 10/017,676	Applicant(s)/Patent Under Reexamination ABDOLLAHI-ALIBEIK ET AL.	
	Examiner Pierre-Michel Bataille	Art Unit 2186	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,665,297	12-2003	Hariguchi et al.	370/392
	B	US-5,938,736	08-1999	Muller et al.	709/243
	C	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Miyatake, H et al. A design for high-speed low-power CMOS fully parallel content-addressable memory macros, IEEE Journal of Solid-State Circuits, Volume: 36, Issue: 6, pp. 956 - 968, June 2001.
	V	McAuley, A. et al., Fast routing table lookup using CAMs, IEEE, pp. 1382-1391, 1993.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.